

# 2-WAY POWER DIVIDER/COMBINER

## CONNECTORIZED MODEL: CSBK260S

**WIDE BANDWIDTH**

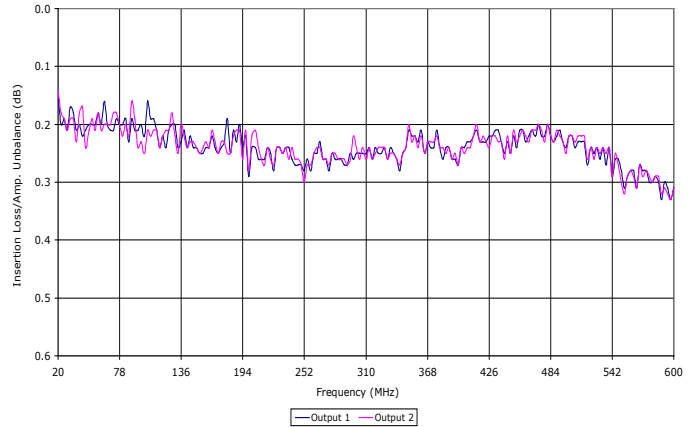
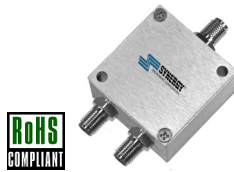
**20 - 600 MHz**

**FEATURES:**

- ▶ Excellent Performance
- ▶ Low Power

**APPLICATIONS:**

- ▶ Signal Splitting & Low Power Combining



**SPECIFICATIONS (Rev C 09/28/17)**

<b>Frequency</b>	20 - 600 MHz		
<b>Insertion Loss (dB)<sup>1</sup></b>	Frequency (MHz)	Typ.	Max.
	20 - 40	0.25	0.35
	40 - 300	0.36	0.45
	300 - 600	0.40	0.75
<b>Isolation (dB)</b>	Frequency (MHz)	Typ.	Min.
	20 - 40	23	20
	40 - 300	25	20
	300 - 600	23	20
<b>Phase Unbalance (Deg.)</b>	Frequency (MHz)	Typ.	Max.
	20 - 40	0.5	2.0
	40 - 300	0.8	2.0
	300 - 600	0.8	3.0
<b>Amplitude Unbalance (dB)</b>	Frequency (MHz)	Typ.	Max.
	20 - 40	0.05	0.20
	40 - 300	0.05	0.20
	300 - 600	0.05	0.20
<b>VSWR</b>	1.15:1 (Typ.) / 1.40:1 (Max.)		
<b>Combined Power @ Port 1<sup>2</sup></b>	50 Watt (CW)		
<b>Operating Temperature Range</b>	-40 to +85 °C		

<sup>1</sup> Insertion loss is above the theoretical 3.0 dB splitting loss.  
<sup>2</sup> Handling power assumes ideal 50 Ohms impedance matching on all ports.

**Package # 437**

All Dimensions in inches (mm)

Tolerances:  
 2 PL. ±0.030 inch. (0.762mm)  
 3 PL. ±0.015 inch. (0.381mm)

**Port Configuration**

Sum Port	Input / Output
1	2,3

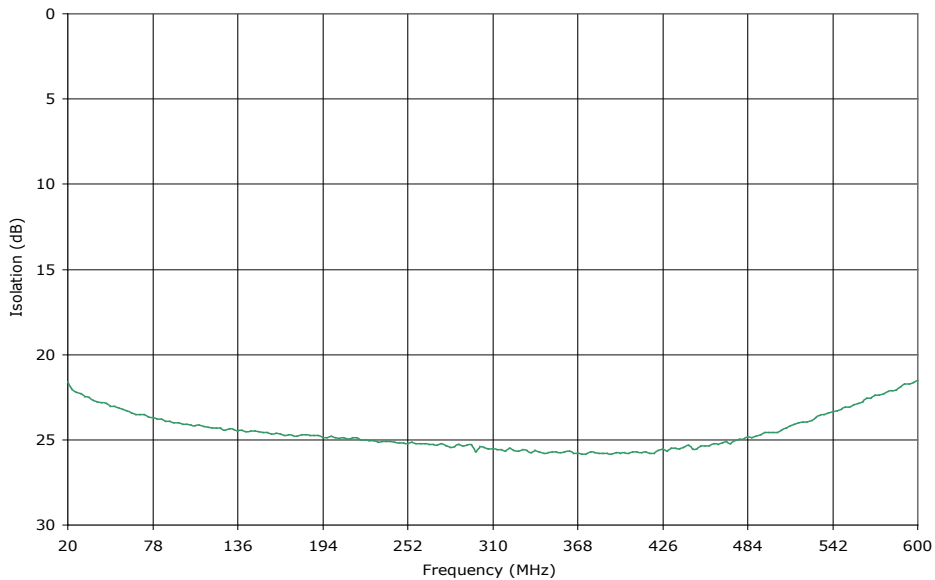
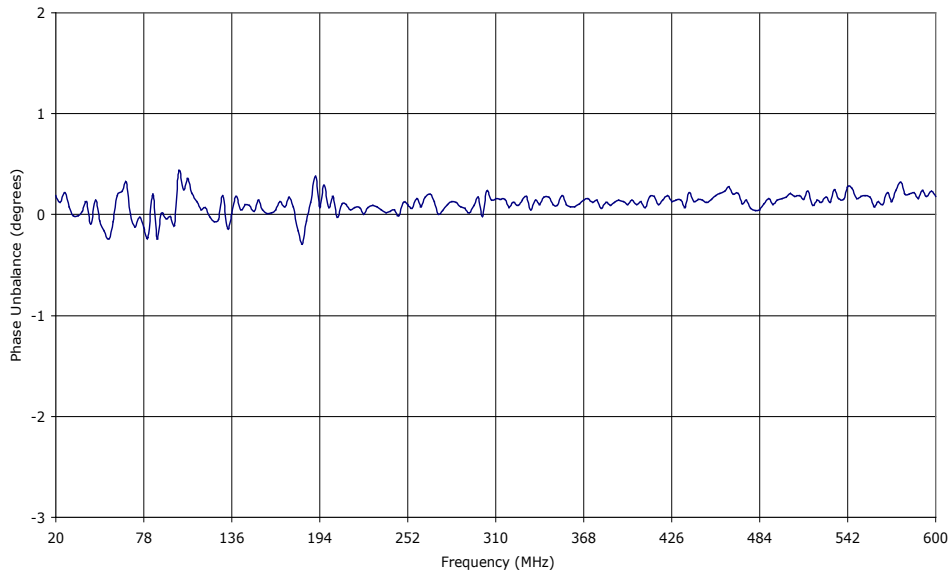
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Data @ 25 °C



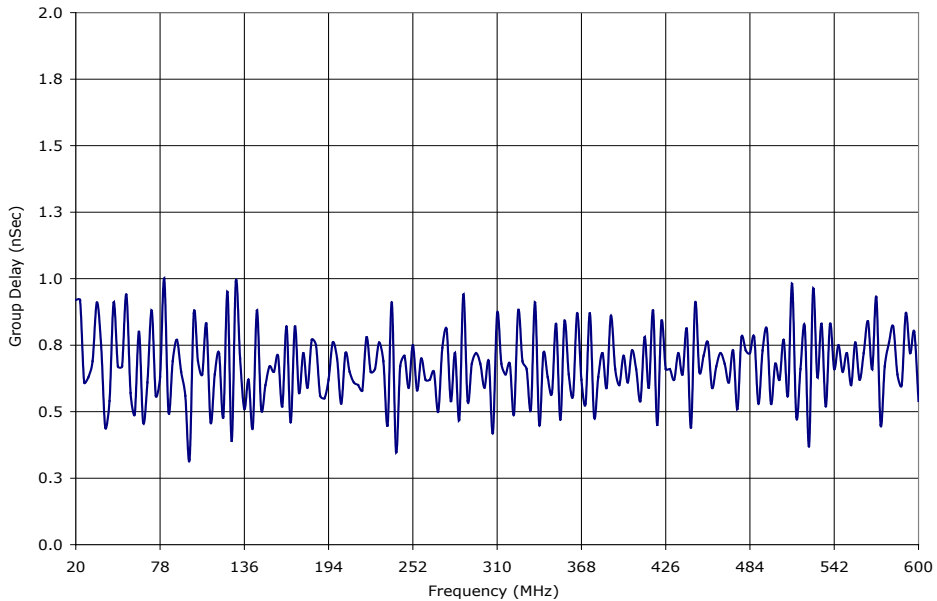
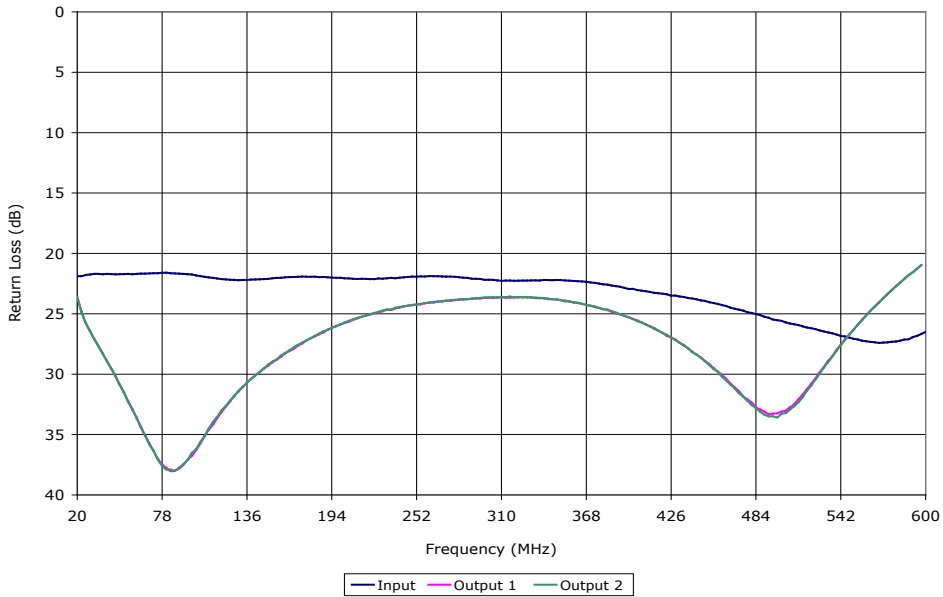
PERFORMANCE PLOTS

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